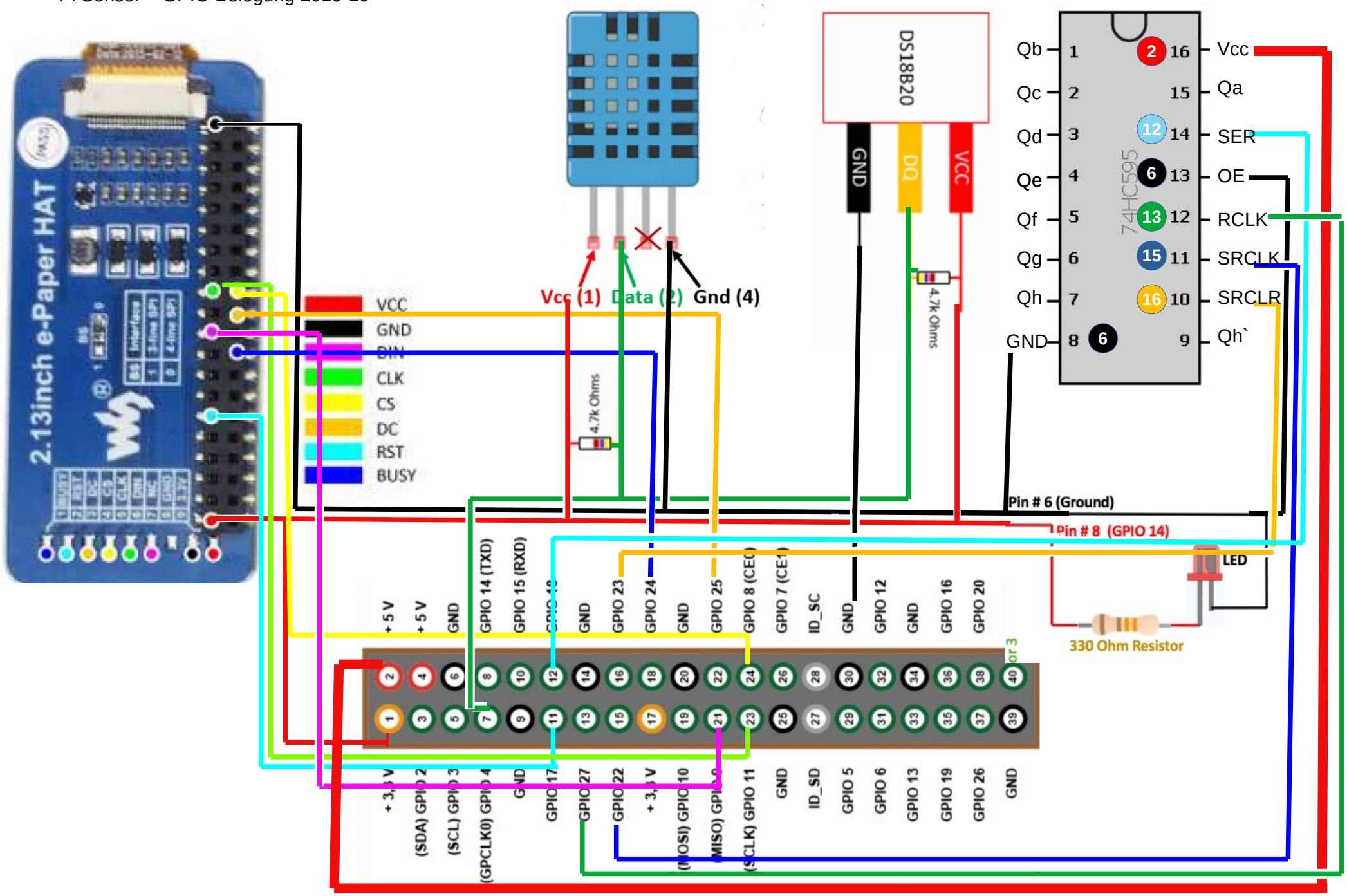
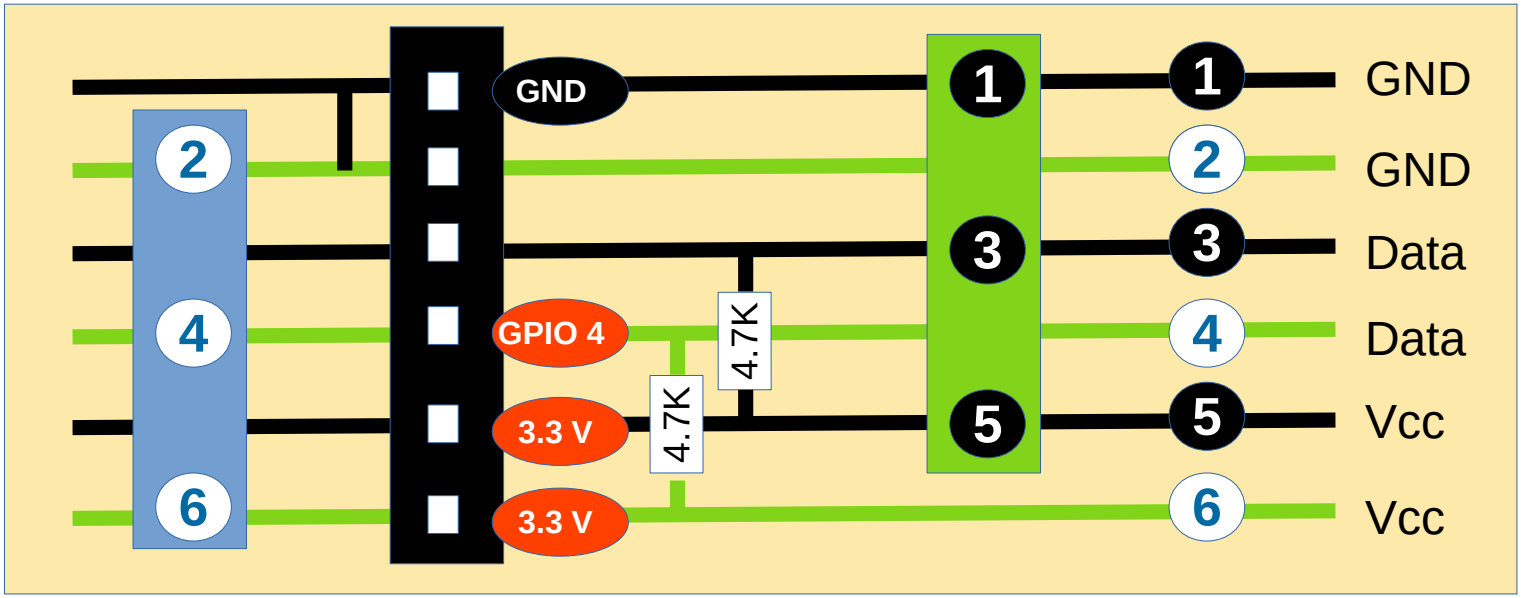
Pi Sensor – GPIO Belegung 2019-10



Multiplexer SN74HC595N

Pin-Name	Pin-Nr	GPIO-Board	Beschreibung	Typ
GND	8	6	Ground Pin	Input
OE	13	6	Output Enable is negative logic too: When the voltage on it is HIGH, the output pins are disabled/set to high impedance state and don't allow current to flow. When OE gets low voltage, the output pins work normally.	Input
Q_A	15		Signal Out are the output pins and should be connected to some type of output like LEDs, 7 Segments etc.	Output
Q_B	1			
Q_C	2			
Q_D	3			
Q_E	4			
Q_F	5			
Q_G	6			
Q_H	7			
$Q_{H`}$	9		Pin outputs bit 7 of the ShiftRegister. It is there so that we may daisychain 595s: if you connect this QH' to the SER pin of another 595, and give both ICs the same clock signal, they will behave like a single IC with 16 outputs. Of course, this technique is not limited to two ICs – you can daisychain as many as you like, if you have enough power for all of them.	Output
RCLK	12	13	Register Clock / Latch is a very important pin. When driven HIGH, the contents of Shift Register are copied into the Storage/Latch Register; which ultimately shows up at the output. So the latch pin can be seen as like the final step in the process to seeing our results at the output, which in this case are LEDs.	Input
SER	14	12	Serial pin is used to feed data into the shift register a bit at a time.	Input
SRCLK	11	15	Shift Register Clock is the clock for the shift register. The 595 is clock-driven on the rising edge. This means that in order to shift bits into the shift register, the clock must be HIGH. And bits are transferred in on the rising edge of the clock	Input
SRCLR	10	16	Shift Register Clear pin allows us to reset the entire Shift Register, making all its bits 0, at once. This is a negative logic pin, so to perform this reset; we need to set the SRCLR pin LOW. When no reset is required, this pin should be HIGH.	Input
VCC	16	2	Power	Input

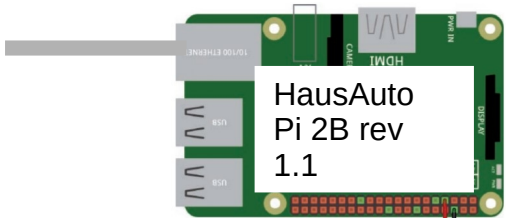
Platine Anschluss DS18B20 und DHT11 an SensorPi



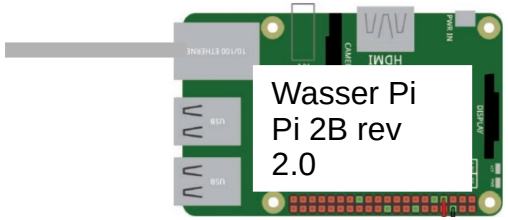
DS18B20 To GPIO DHT11

Schema Hausautomation

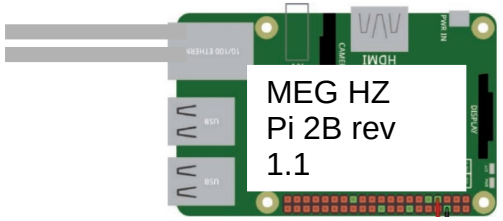
Betrieb



Lan 192.168.1.44
OG Schlafen Sigrid



Lan 192.168.1.xx
Wlan 192.168.1.xx
UG Basteln



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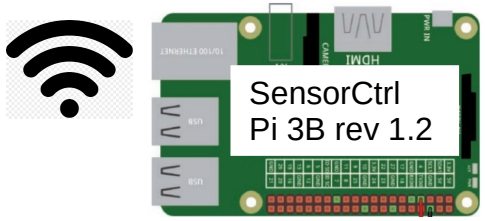
HZ Lan 192..188.101.2
UG Technik



Lan 192.168.1.46
Wlan 192.168.1.47
UG Basteln

Experiment